

EAST Search History

Ref #	Hits	Search Query	Dbs	Default Operator	Plurals	Time Stamp
L14	21	hardware with (implement\$6) near2 (orb object adj request adj broker)	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 14:25
L15	64	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker)	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 14:33
L16	0	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and pluggable	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 14:33
L17	15	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and corba	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 14:33
L18	9	17 not 14	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 14:34
L19	7	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and sca	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 14:36
L20	10	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and waveform	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 14:47
L21	7	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and (sca jttrs)	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 14:51
L22	11	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and (sca jttrs waveform)	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 14:51
L23	11	(asic fpga hardware chip circuit dsp) near3 (orb object adj request adj broker) and (sca jttrs waveform)	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 15:06
L24	16	(asic fpga hardware chip circuit dsp gate) near3 (orb object adj request adj broker) and (sca jttrs waveform)	US-PGPUB; USPAT; USCCR; EPO; DEWENT; IBM_TDB	OR	ON	2008/11/05 15:06